

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant :	Hidetoshi Nishikawa	Art Unit :	2819
Serial No. :	10/561,552	Examiner :	Vibol Tan
Filed :	March 20, 2006	Conf. No. :	9795
Title :	SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE		

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

PRELIMINARY AMENDMENT

Prior to examination, please amend the application as indicated on the following pages.

CERTIFICATE OF MAILING BY EFS-WEB FILING

I hereby certify that this paper was filed with the Patent and Trademark Office using the EFS-WEB system on this date: May 22, 2008